



IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant:

Yoshinobu Ono et al.

Conf.:

2546

Appl. No.:

10/046,741

Group:

2812

Filed:

January 17, 2002

Examiner: S. Mulpuri

For:

METHOD FOR FABRICATING III-V COMPOUND

SEMICONDUCTOR

AMENDMENT UNDER 37 C.F.R. § 1.111

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 July 19, 2004 (Monday)

Sir:

In reply to the Office Action dated March 18, 2004, the period for response having been extended by one (1) month until July 18, 2004, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

This reply includes amendments to the specification, a declarataion and remarks.

07/20/2004 CCHRU1 00000104 10046741

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